



N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/632224	Docket Number	CNTR.2141			
Filed	7/31/2003	Group Art Unit	2183			
Examiner	EDDIE CHAN	Customer No.	23669			
Application Title	APPARATUS AND METHOD FOR KILLING AN INSTRUCTION AFTER LOADING THE INSTRUCTION INTO AN INSTRUCTION QUEUE IN A PIPELINED MICROPROCESSOR					
First Named Inventor	THOMAS C. MCDONALD					

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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Respectfully submitted.

E. ALAN DAVIS Reg. No. 39,954 Tel.: (512) 301-7234

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7-9-05 Date



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Examiner Name	EDDIE CHAN				
Attorney Docket Number	CNTR.2141				

···········			U.S. PATEN	T DOCUMENTS	
Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No. ¹	Number-Kind Code ^{2(if known)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
		5706491	01-06-1998	Steven C. McMahan	
		5313634	05-17-1994	James Eickemeyer	
		5961629	10-05-1999	Nguyen et al	
		5832289	11-03-1998	Shaw et al	
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		6233676	05-15-2001	Henry et al	
		5623615	04-22-1997	Salem et al	
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		6260138	07-10-2001	Jeremy G. Harris	
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		6256727	07-03-2001	Robert Greg McDonald	
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		5881260	03-09-1999	Raje et al	
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		5142634	08-25-1992	Fite et al	
		5163140	11-10-1992	Stiles et al	
		5404467	04-04-1995	Saba et al	

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				Application Number	10/632224
INFORM	IATION DISC	LOS	URE	Filing Date	7/31/2003
STATEMENT BY APPLICANT			ANT	First Named Inventor	THOMAS C. MCDONALD
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		5687360	11-11-1997	Po-Hua Chang		
		5761723	06-02-1998	Black et al		
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		5964868	10-12-1999	Gochman et al		
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		5687349	11-11-1997	Ralph C. McGarity		
		6279105	08-21-2001	Konigsburg et al		

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					Appear
	<u> </u>	5968169	10-19-1999	James K. Pickett	
		6085311	07-04-2000	Narayan et al	
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		20050076193	04-07-2005	Henry et al.	

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Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. **Microprocessor Report.** Vol.9. No.2. February 16, 1995. page 5 **Microprocessor Report.** August 23, 1999. page 7 **YEH ET AL.** Alternative Implementation of Two-Level Adaptive Branch Prediction.** 19th Annual International Symposium on Computer Architecture. Pp124-134. May 19-21, 1992. Gold Coast, Australia	T²
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